

WHAT IS CLAIMED IS:

1. A system for testing electronic modules comprising:
 - 5 at least one mapping board box including at least one harness port; at least one harness operably connected at one end to the at least one harness port,
 - 10 the mapping board box including a plurality of pin receptors in electronic communication with the at least one harness port, the pin receptors adapted to communicate with a circuit printed on a circuit board with pins, wherein the mapping board box is pre-wired to receive circuit boards with a variety of pin configurations.
 - 15 2. The system of claim 1 wherein the dimensions of the at least one mapping board box are sized to fit a printed circuit board.
 3. The system of claim 1 wherein the at least one harness port comprises 56 pins.
 - 20 4. The system of claim 1 wherein the at least one mapping board box comprises 560 pin receptors.
 5. The system of claim 1 wherein the at least one harness comprises a generic harness.
 - 25 6. The system of claim 5 where in the generic harness comprises 56 wires.
 7. The system of claim 1 further comprising an electronic simulators.

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8. A method of testing an electronic module, the method comprising pinning a circuit board corresponding to an electronic module; and inserting the pinned circuit board into a pre-wired mapping board
5 box.

9. The method of claim 8, the method further comprising receiving the pinned portion of the board into connection ports in a mapping board box.

10 10. The method of claim 9, the method further comprising communicating between a circuit and the module via the mapping board box and a harness connection.

11. A system for testing an electronic module comprising:
15 means for receiving a circuit board;
means for communicating between the circuit board receiving means and an electronic module.